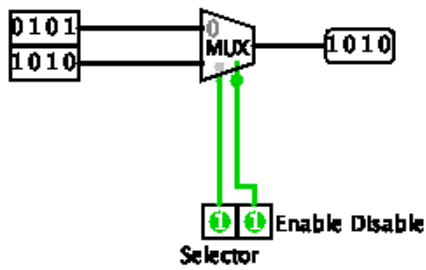


ASDV 1205, LAB

1. Design a MUX, 2 inputs bit-width 4, selector.  
Test it for inputs 10 and 5 decimal



2. Design a multiplexer with 8 inputs, and data bit-width 8.  
Export png that selects input3(4<sup>th</sup> from top), where input3 has the value 9 in decimal.
3. Design a DMUX, input 8 data-bit 1, Export png with selector being 7 and input being 1.
4. Design the following D-flip-flop:

